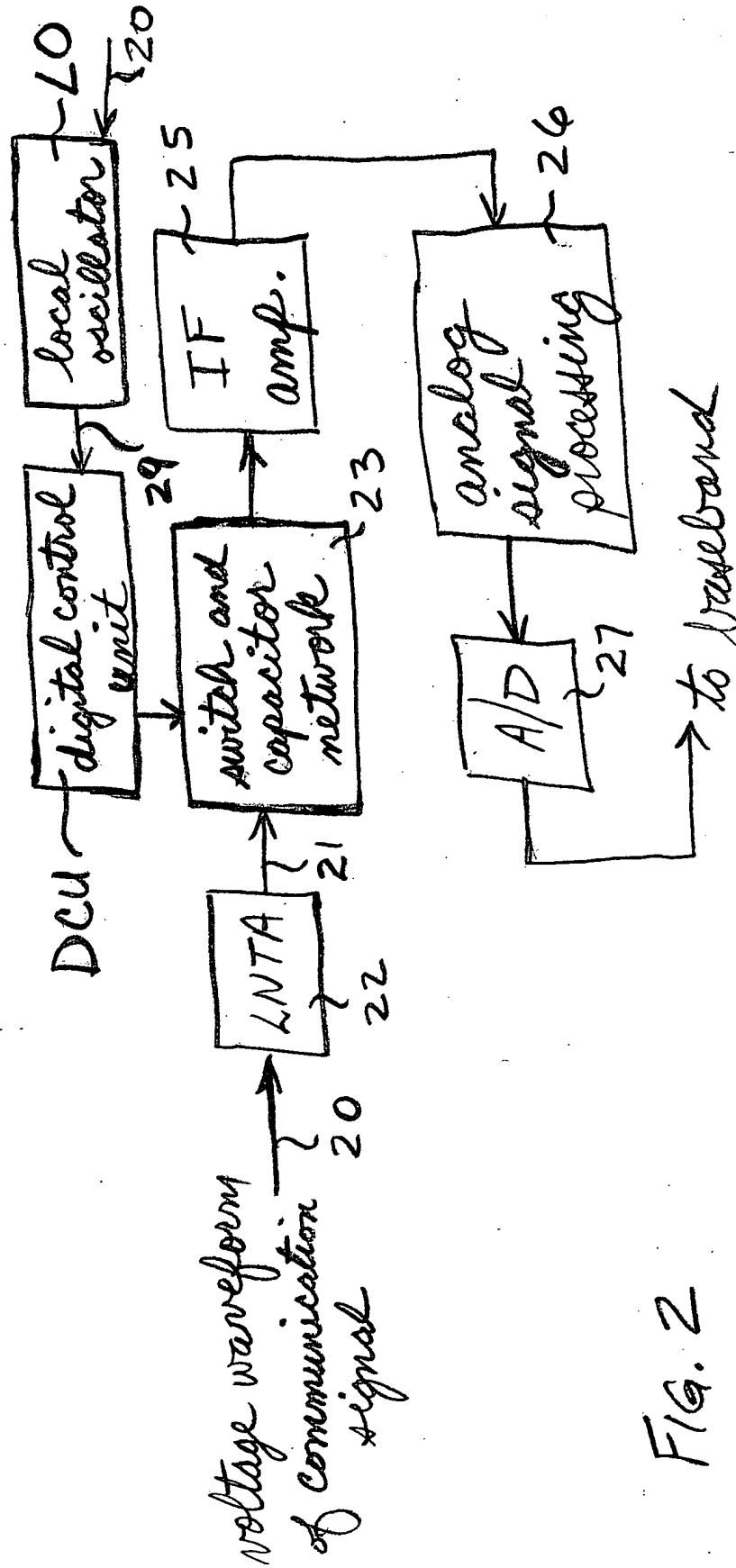
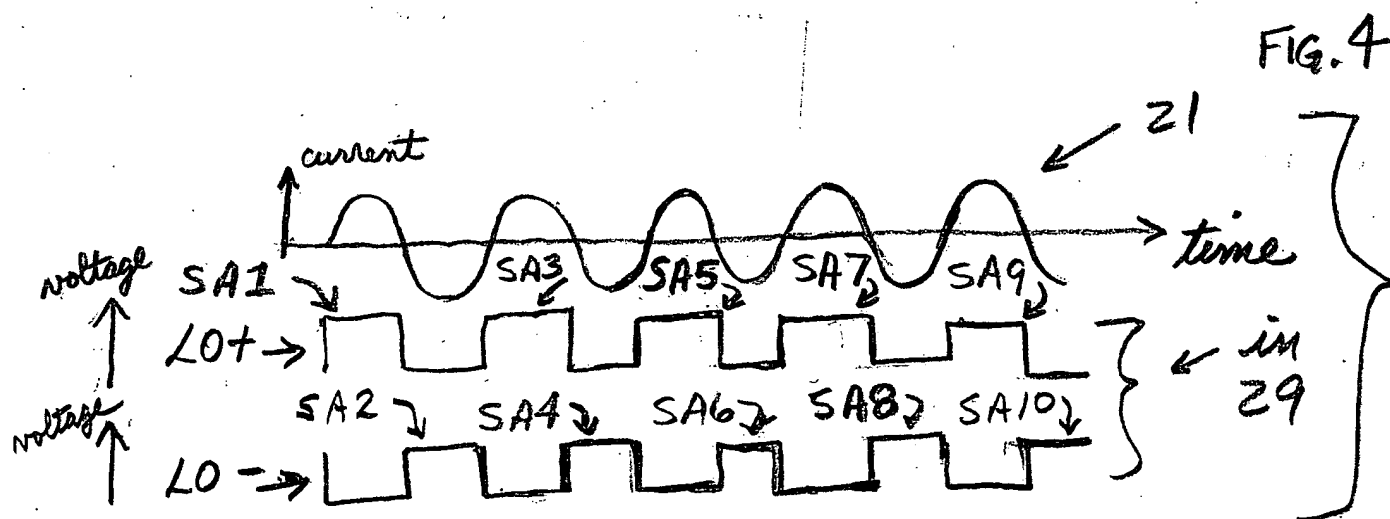
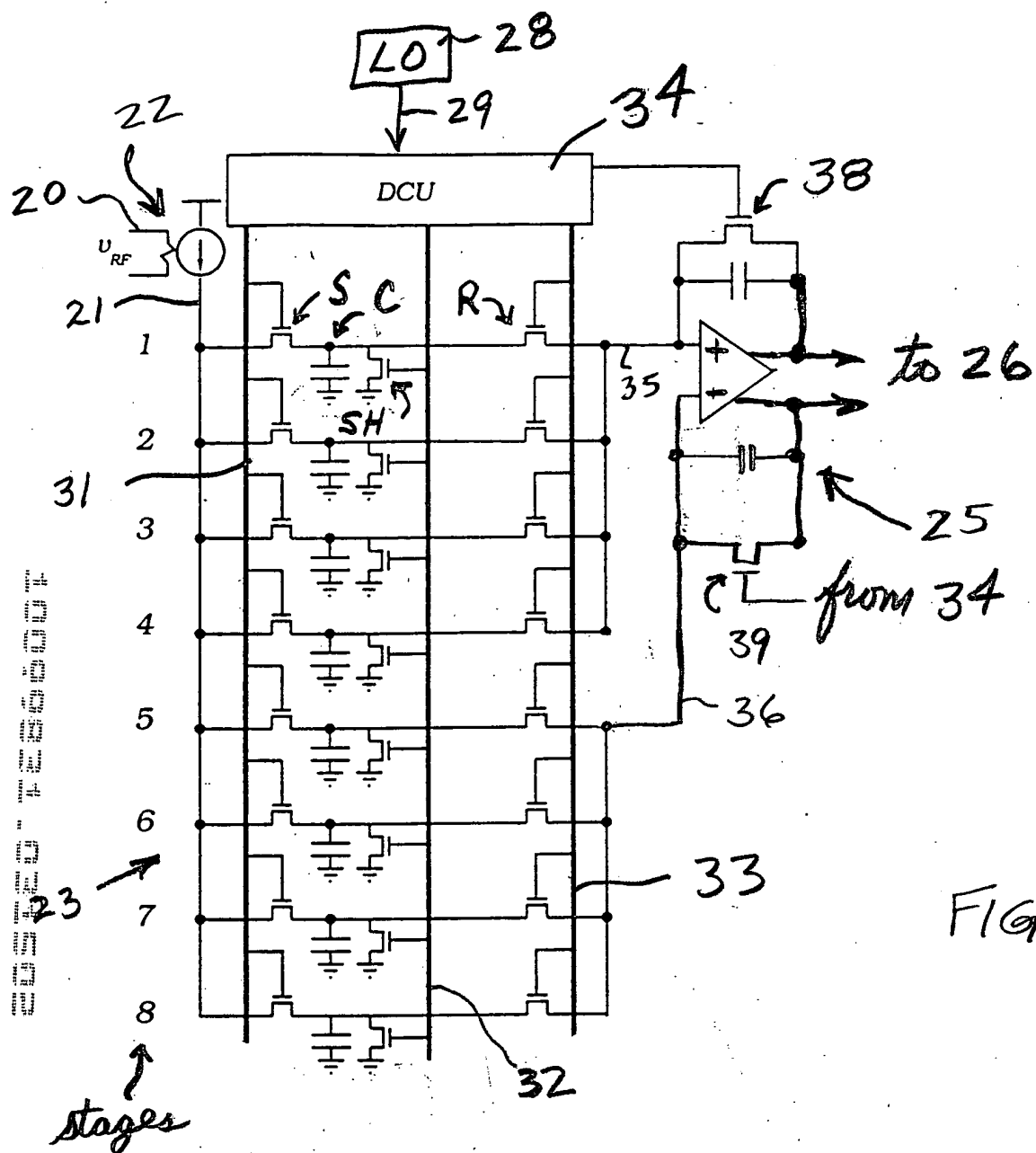


FIG. 1 PRIOR ART





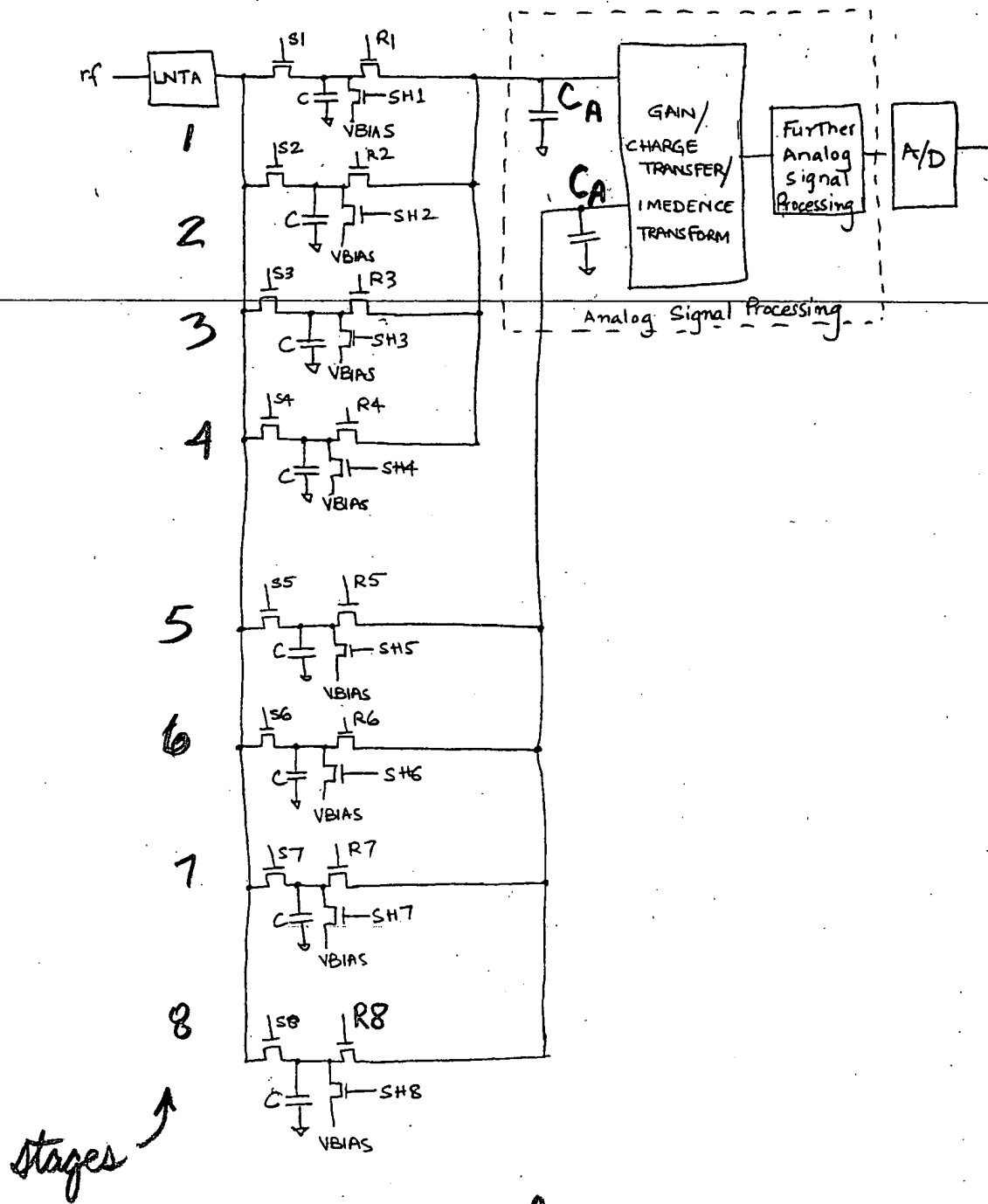
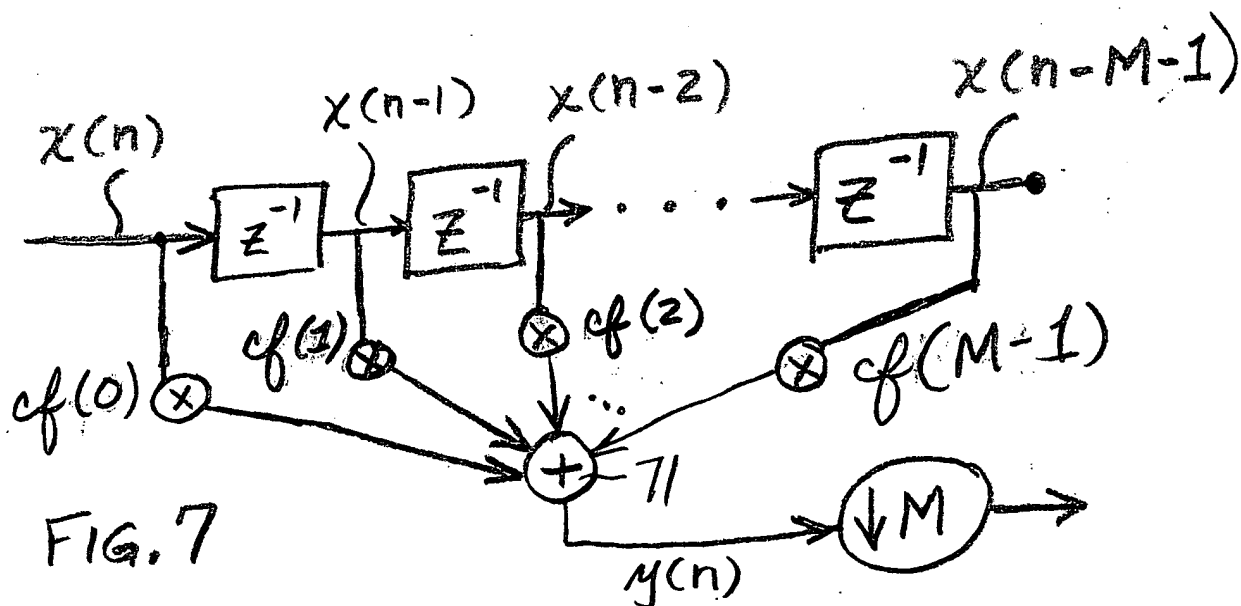


Fig 3A

Sample	Switches Closed
SA1	S1 and R8
SA2	S5 and R1
SA3	S2 and R5
SA4	S6 and R2
SA5	S3 and R6
SA6	S7 and R3
SA7	S4 and R7
SA8	S8 and R4
SA9	S1 and R8
SA10	S5 and R1
SA11	S2 and R5
⋮	⋮

FIG. 5



Sample	Switches closed for Sampling	Switches closed for Read Out
SA1	S1	R3, R4, R7, R8
SA2	S5	-do-
SA3	S2	-do-
SA4	S6	-do-
SA5	S3	R1, R2, R5, R6
SA6	S7	-do-
SA7	S4	-do-
SA8	S8	-do-
SA9	S1	R3, R4, R7, R8
SA10	S5	-do-
...	...	...

FIG. 5A


SAMPLE	SWITCHES CLOSED
SA1	S1 <i>and R4 and R8</i>
SA2	S5 <i>and R8</i>
SA3	S1
SA4	S5 and R1
SA5	S2 and R1 and R5
SA6	S6 and R5
SA7	S2
SA8	S6 and R2
SA9	S3 and R2 and R6
SA10	S7 and R6
SA11	S3
SA12	S7 and R3
SA13	S4 and R3 and R7
SA14	S8 and R7
SA15	S4
SA16	S8 and R4
SA17	S1 and R4 and R8
SA18	S5 and R8
SA19	S1
SA20	S5 and R1
SA21	S2 and R1 and R5
•	•
•	•
•	•

FIGURE 6

Sample	Switches closed for Sampling	Switches closed for Read Out
SA1	S1	R3, R4, R7, R8
SA2	S5	-do-
SA3	S1	-do-
SA4	S5	-do-
SA5	S2	-do-
SA6	S6	-do-
SA7	S2	-do-
SA8	S6	-do-
SA9	S3	R1, R2, R5, R6
SA10	S7	-do-
SA11	S3	-do-
SA12	S7	-do-
SA13	S4	-do-
SA14	S8	-do-
SA15	S4	-do-
SA16	S8	-do-
SA17	S1	R3, R4, R7, R8
SA18	S5	-do-
SA19	S1	-do-
SA20	S5	-do-
...	...	...

FIG. 6A

Sample	Switches Closed
SA1	S1, S2, R4 and R7
SA2	S5, S6, R1 and R8
SA3	S3, S4, R2 and R5
SA4	S7, S8, R6 and R3
SA5	S1, S2, R4 and R7
SA6	S5, S6, R8 and R1
SA7	S3, S4, R2 and R5
SA8	S7, S8, R6 and R3
⋮	⋮



$$\frac{1}{2}x(n) + \frac{1}{2}x(n-1)$$

FIG. 8

Sample	Switches closed for Sampling	Switches closed for Read Out
SA1	S1	R5', R6, R7, R8'
SA2	S9	-do-
SA3	S2	-do-
SA4	S10	-do-
SA5	S3	R7', R8, R1, R2'
SA6	S11	-do-
SA7	S4	-do-
SA8	S12	-do-
SA9	S5	R1', R2, R3, R4'
SA10	S13	-do-
SA11	S6	-do-
SA12	S14	-do-
SA13	S7	R3', R4, R5, R6'
SA14	S15	-do-
SA15	S8	-do-
SA16	S16	-do-
SA17	S1	R5', R6, R7, R8'
SA18	S9	-do-
SA19	S2	-do-
SA20	S10	-do-
...	...	...

Fig. 8A

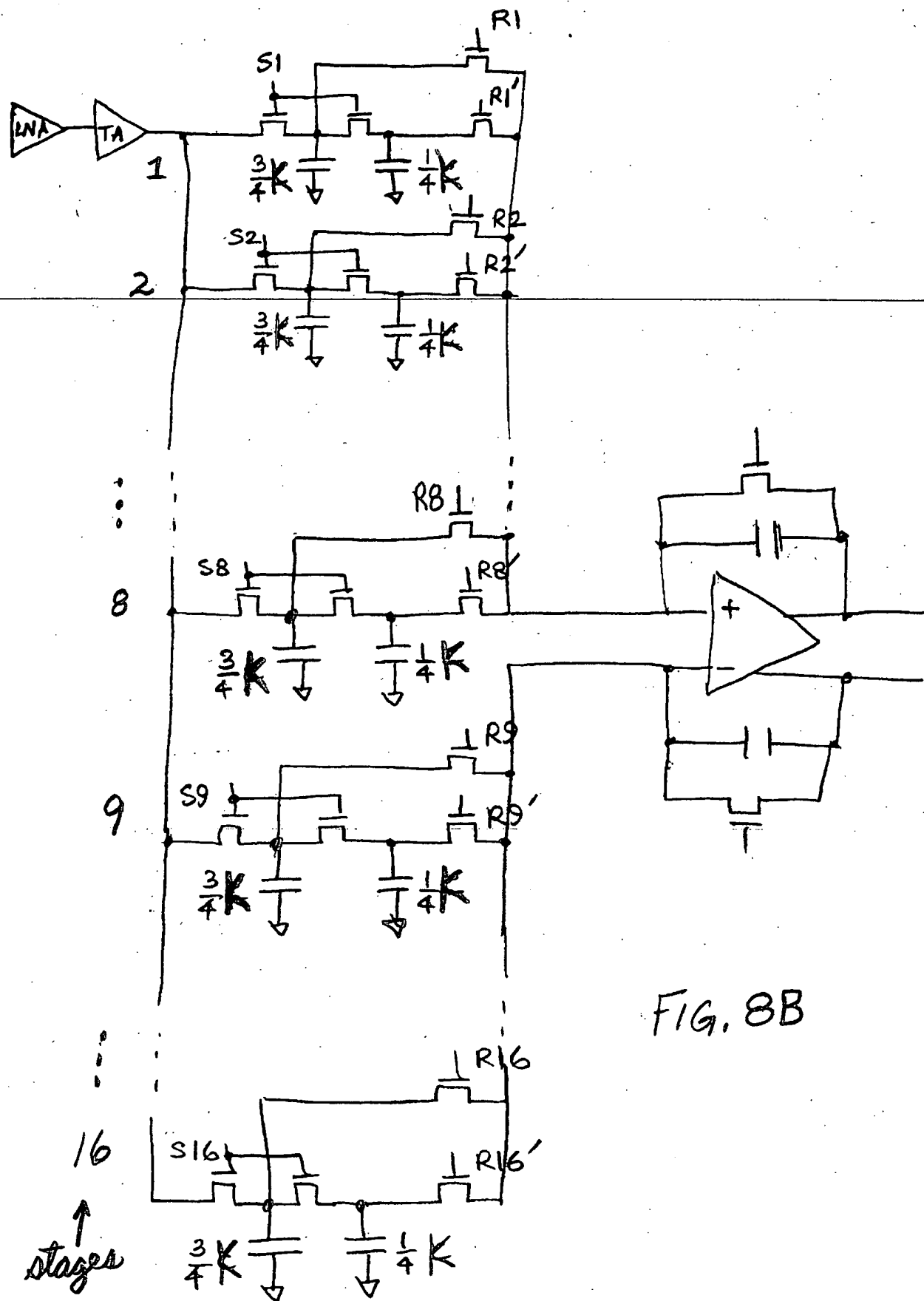
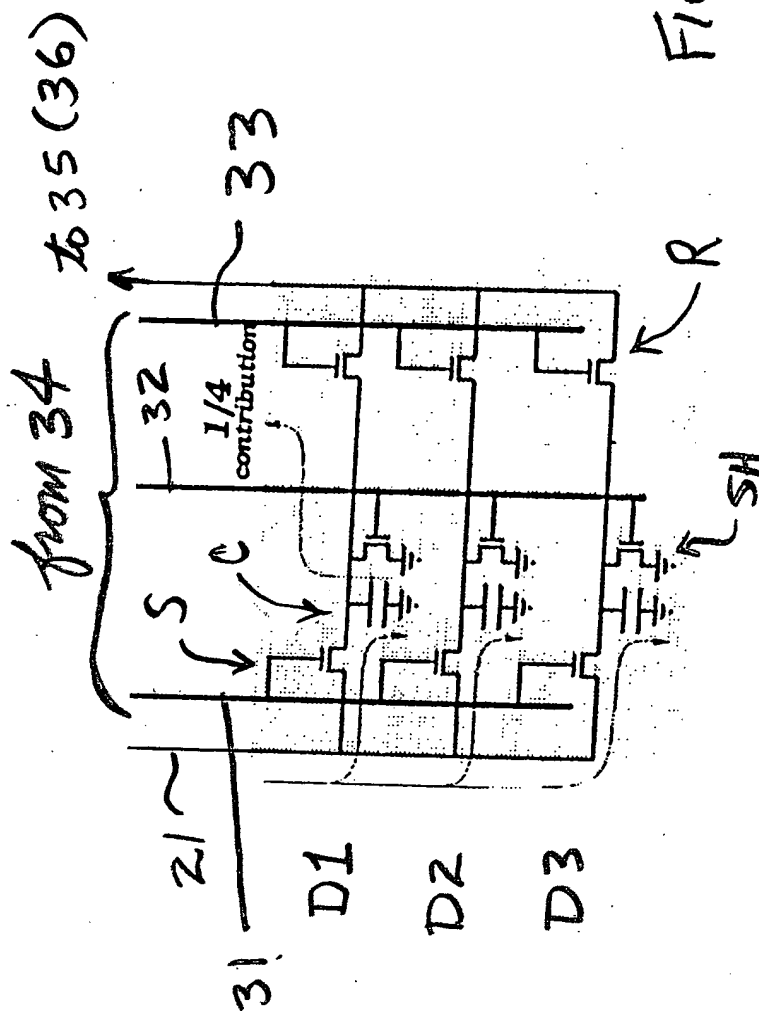
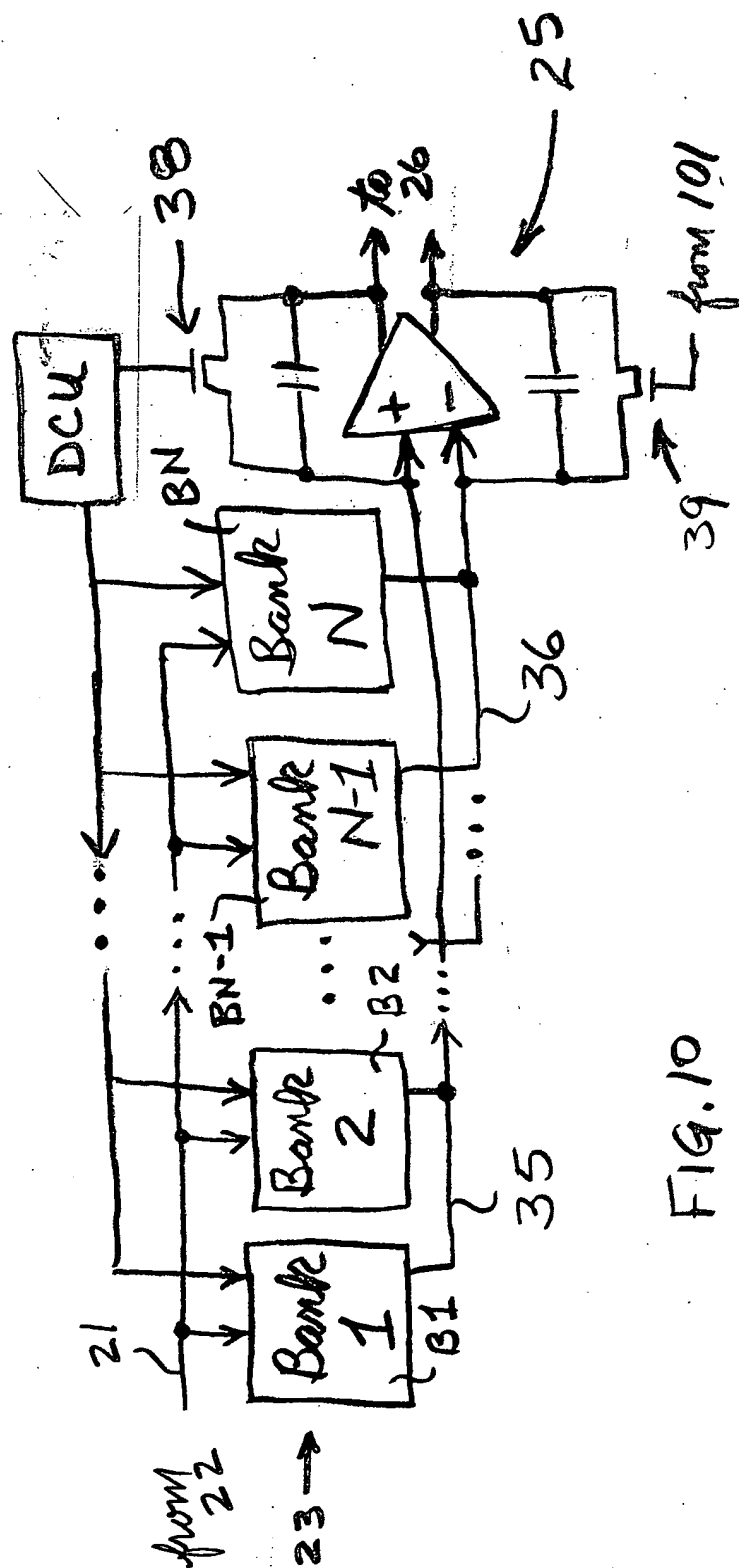


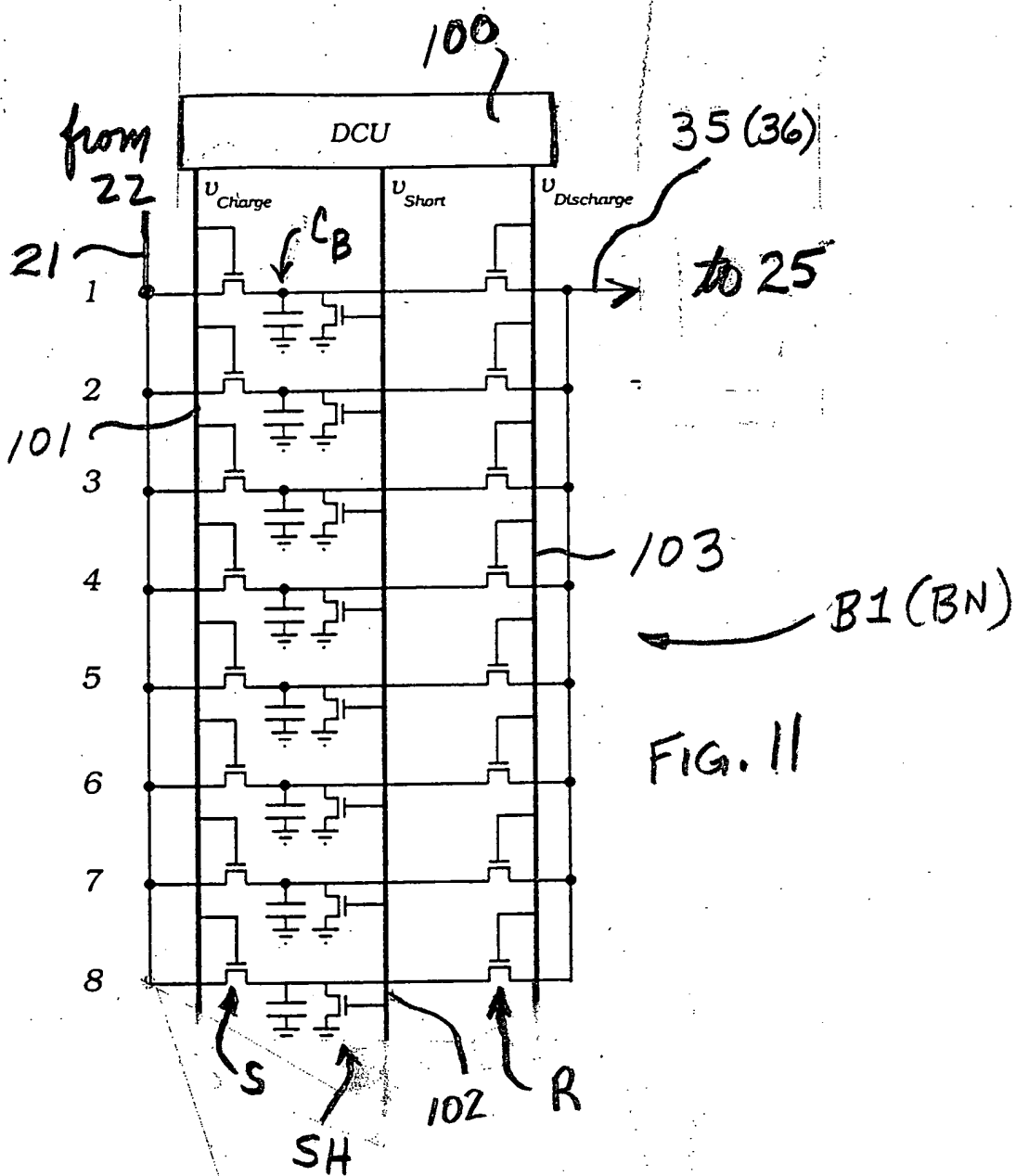
FIG. 8B

5





1910



Sample	S1-S8 closed	R1 & R2 closed	R3 closed	SH1-SH8 closed
SA1	B1	—	—	—
SA2	B5	B1	—	—
SA3	B2	B5	B1	—
SA4	B6	B2	B5	B1
SA5	B3	B6	B2	B5
SA6	B7	B3	B6	B2
SA7	B4	B7	B3	B6
SA8	B8	B4	B7	B3
SA9	B1	B8	B4	B7
SA10	B5	B1	B8	B4
SA11	B2	B5	B1	B8
SA12	B6	B2	B5	B1
⋮	⋮	⋮	⋮	⋮



$$\frac{1}{4}x(n) + \frac{1}{8}x(n-1)$$

FIG. 12



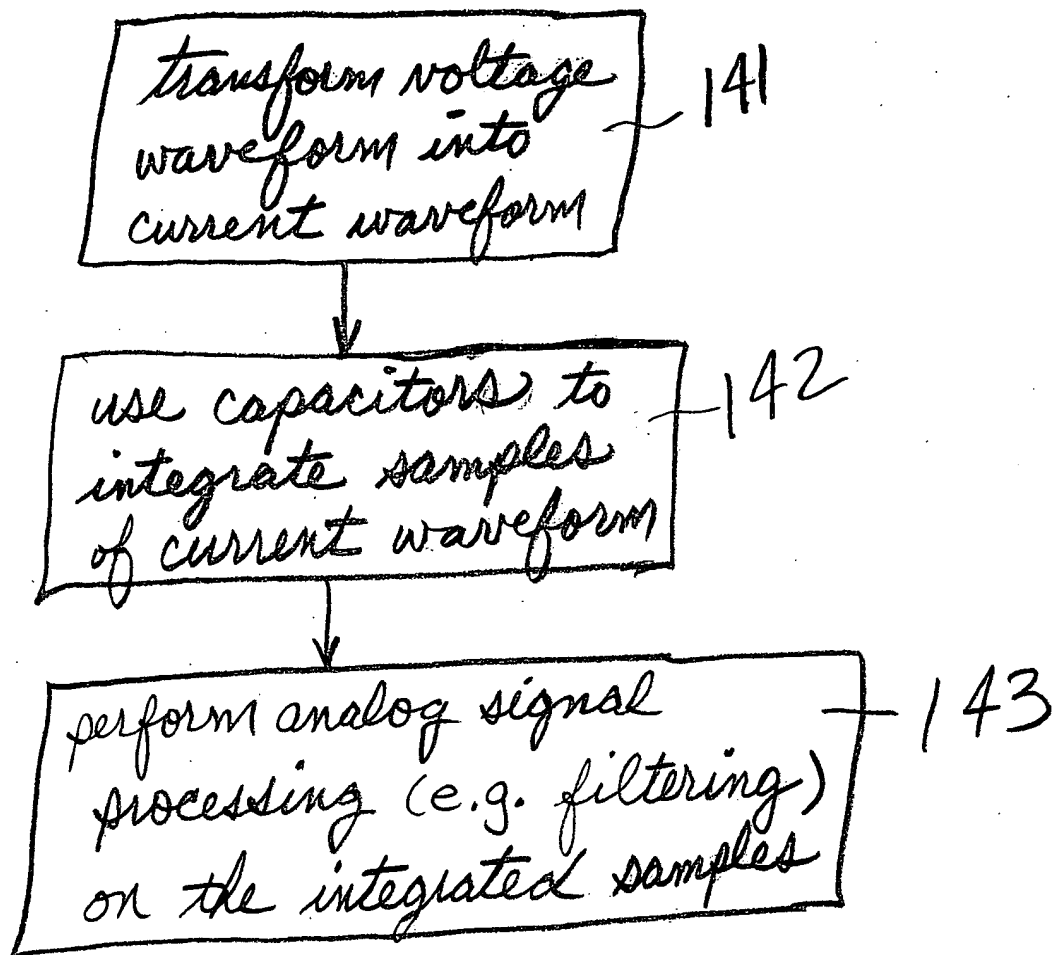


FIG. 14

**FIG. 15A**

**FIG. 15B**

**FIGURE 15**

**FIG. 17A**

**FIG. 17B**

**FIGURE 17**

SAMPLE	SWITCHES CLOSED
SA1	S1
SA3	S2
SA5	S3
SA7	S1
SA9	S2
SA11	S3
SA13	S1
SA15	S2
SA17	S3
SA19	S4 and R1
SA21	S2 and R1
SA23	S3 and R1
SA25	S4 and R1
SA27	S2 and R1
SA29	S3 and R1
SA31	S4 and R1
SA33	S2 and R1
SA35	S3 and R1
SA37	S4 and R2
SA39	S1 and R2
SA41	S3 and R2
SA43	S4 and R2
SA45	S1 and R2
SA47	S3 and R2
SA49	S4 and R2
SA51	S1 and R2
SA53	S3 and R2
SA55	S4 and R3
SA57	S1 and R3
SA59	S2 and R3
SA61	S4 and R3
SA63	S1 and R3
SA65	S2 and R3
SA67	S4 and R3
SA69	S1 and R3
SA71	S2 and R3
SA73	S3 and R4
SA75	S1 and R4
SA77	S2 and R4
SA79	S3 and R4
SA81	S1 and R4
SA83	S2 and R4
SA85	S3 and R4
SA87	S1 and R4
SA89	S2 and R4

**FIGURE 15A**

SA91	S3 and R1
SA93	S4 and R1
SA95	S2 and R1
SA97	S3 and R1
SA99	S4 and R1
SA101	S2 and R1
SA103	S3 and R1
SA105	S4 and R1
SA107	S2 and R1
SA109	S3 and R2
SA111	S4 and R2
SA113	S1 and R2
SA115	S3 and R2
SA117	S4 and R2
SA119	S1 and R2
SA121	S3 and R2
SA123	S4 and R2
SA125	S1 and R2
SA127	S2 and R3
SA129	S4 and R3
SA131	S1 and R3
SA133	S2 and R3
SA135	S4 and R3
SA137	S1 and R3
SA139	S2 and R3
SA141	S4 and R3
SA143	S1 and R3
SA145	S2 and R4
SA147	S3 and R4
SA149	S1 and R4
SA151	S2 and R4
SA153	S3 and R4
SA155	S1 and R4
SA157	S2 and R4
SA159	S3 and R4
SA161	S1 and R4
SA163	S2 and R1
SA165	S3 and R1
SA167	S4 and R1
•	•
•	•
•	•

**FIGURE 15B**

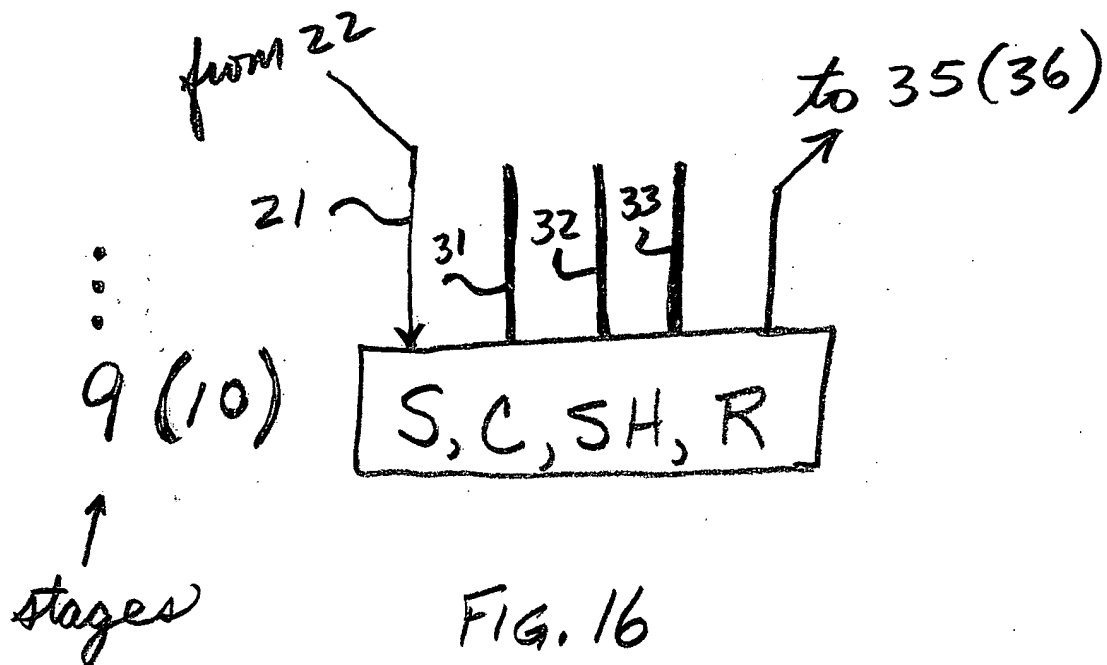


FIG. 16

SA	SWITCHES CLOSED
SA1	S1
SA3	S2
SA5	S3
SA7	S1
SA9	S2
SA11	S3
SA13	S1
SA15	S2
SA17	S3
SA19	S4 and R1
SA21	S2 and R1
SA23	S3 and R1
SA25	S4 and R1
SA27	S2 and R1
SA29	S3 and R1
SA31	S4 and R1
SA33	S2 and R1
SA35	S3 and R1
SA37	S4 and R2
SA39	S1 and R2
SA41	S3 and R2
SA43	S4 and R2
SA45	S1 and R2
SA47	S3 and R2
SA49	S4 and R2
SA51	S1 and R2
SA53	S3 and R2
SA55	S4
SA57	S1
SA59	S2
SA61	S4
SA63	S1
SA65	S2
SA67	S4
SA69	S1
SA71	S2
SA73	S9 and R3 and R4
SA75	S1 and R3 and R4
SA77	S2 and R3 and R4
SA79	S9 and R3 and R4
SA81	S1 and R3 and R4
SA83	S2 and R3 and R4
SA85	S9 and R3 and R4
SA87	S1 and R3 and R4
SA89	S2 and R3 and R4

FIGURE 17A

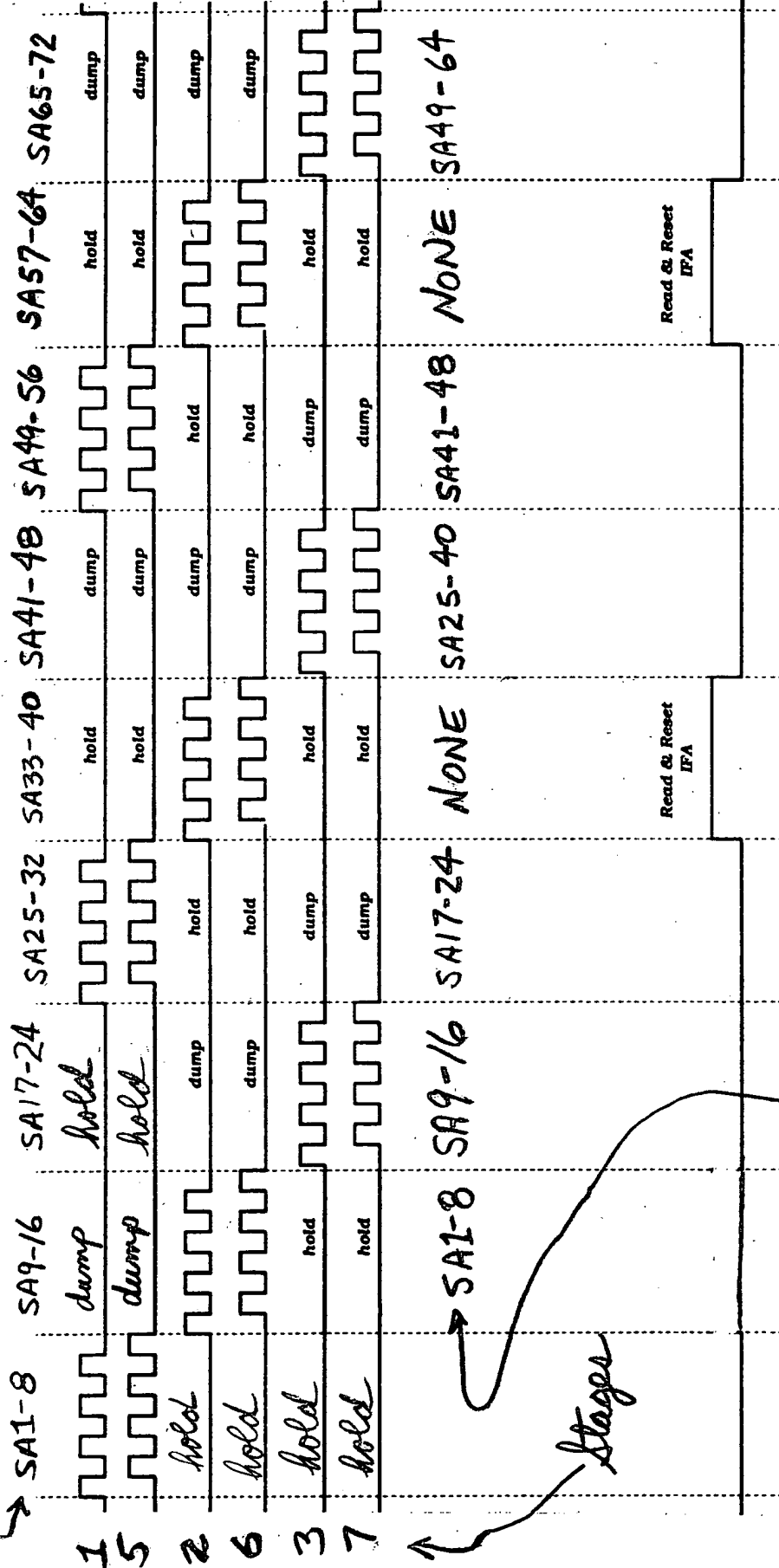
SA91	S2 and R1
SA93	S4 and R1
SA95	S2 and R1
SA97	S9 and R1
SA99	S4 and R1
SA101	S2 and R1
SA103	S9 and R1
SA105	S4 and R1
SA107	S2 and R1
SA109	S9 and R2
SA111	S4 and R2
SA113	S1 and R2
SA115	S9 and R2
SA117	S4 and R2
SA119	S1 and R2
SA121	S9 and R2
SA123	S4 and R2
SA125	S1 and R2
SA127	S2
SA129	S4
SA131	S1
SA133	S2
SA135	S4
SA137	S1
SA139	S2
SA141	S4
SA143	S1
SA145	S2 and R4 and R9
SA147	S3 and R4 and R9
SA149	S1 and R4 and R9
SA151	S2 and R4 and R9
SA153	S3 and R4 and R9
SA155	S1 and R4 and R9
SA157	S2 and R4 and R9
SA159	S3 and R4 and R9
SA161	S1 and R4 and R9
SA163	S2 and R1
SA165	S3 and R1
SA167	S4 and R1
•	•
•	•
•	•

**FIGURE 17B**

SAMPLES	SWITCHES CLOSED
SA1, SA3, ... SA17	S1
SA19, SA21, ... SA35	S2 and R1
SA37, SA39, ... SA53	S3 and R1, R2
SA55, SA57, ... SA71	S4
SA73, SA75, ... SA89	S1 and R2, R3, R4
SA91, SA93, ... SA107	S2 and R1, R3, R4
SA109, SA111, ... SA125	S3 and R1, R2, R4
SA127, SA129, ... SA143	S4
SA145, SA149, ... SA161	S1 and R2, R3, R4
•	•
•	•
•	•

**FIGURE 18**

*Samples Taken*



*Time*

FIG. 19

*Samples Dumped*

*Stages*

202

FIG. 20

201

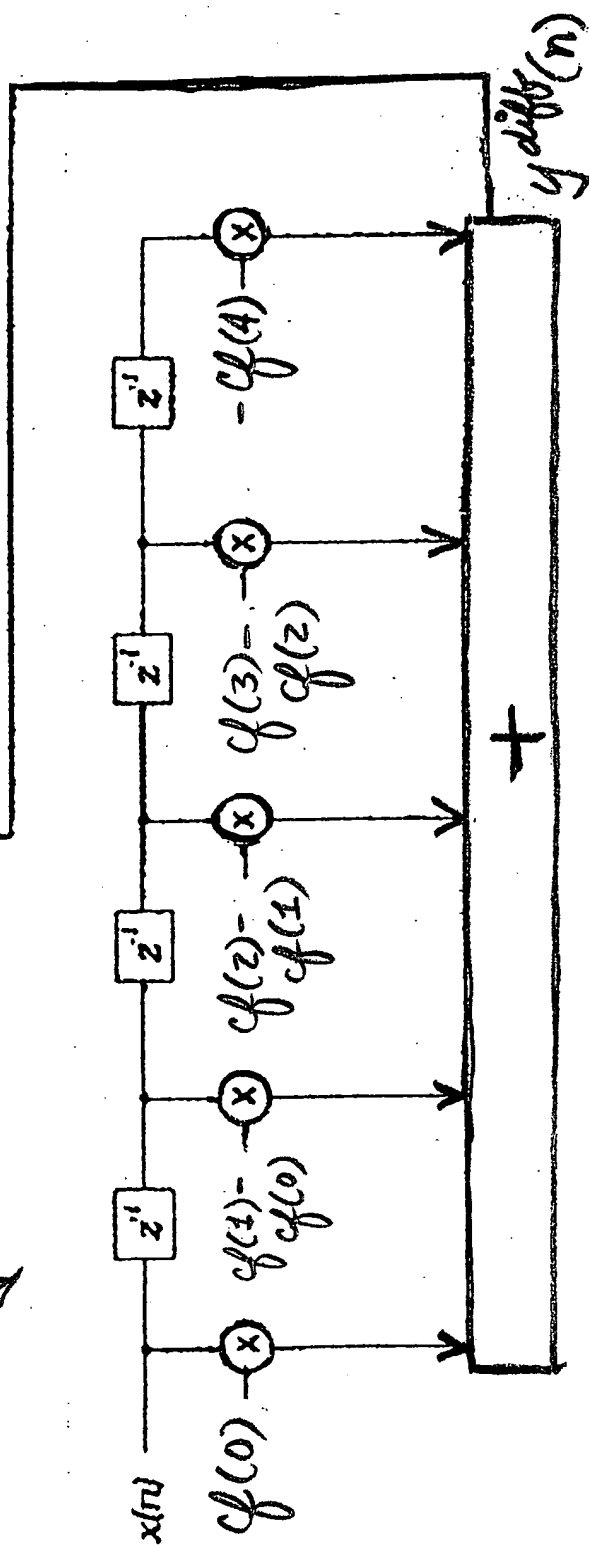
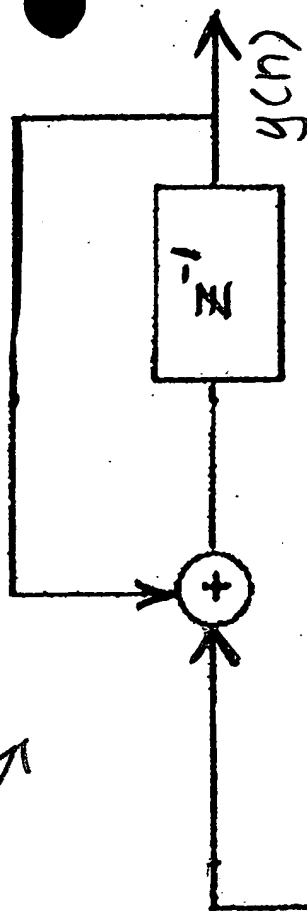


FIG. 20

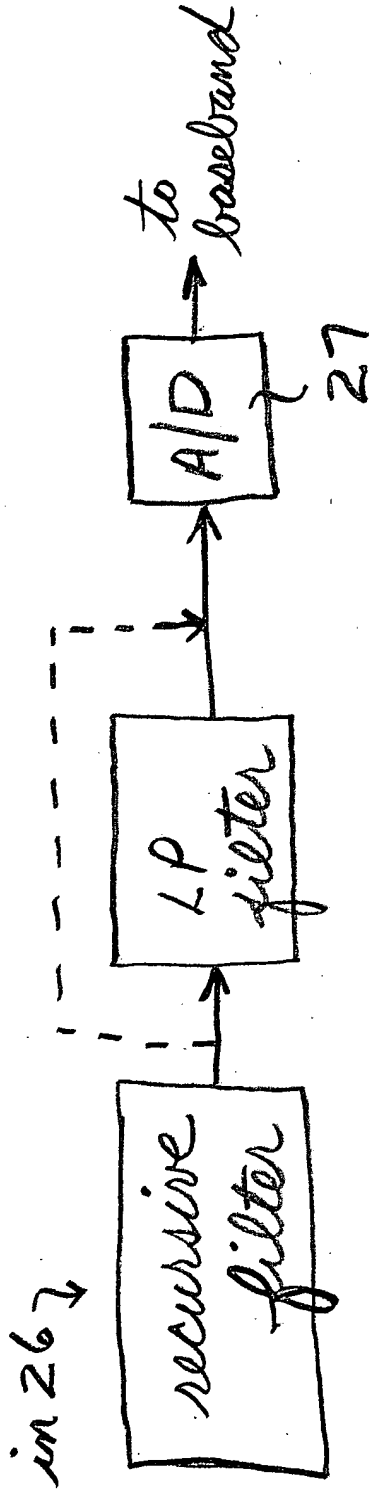


FIG. 21

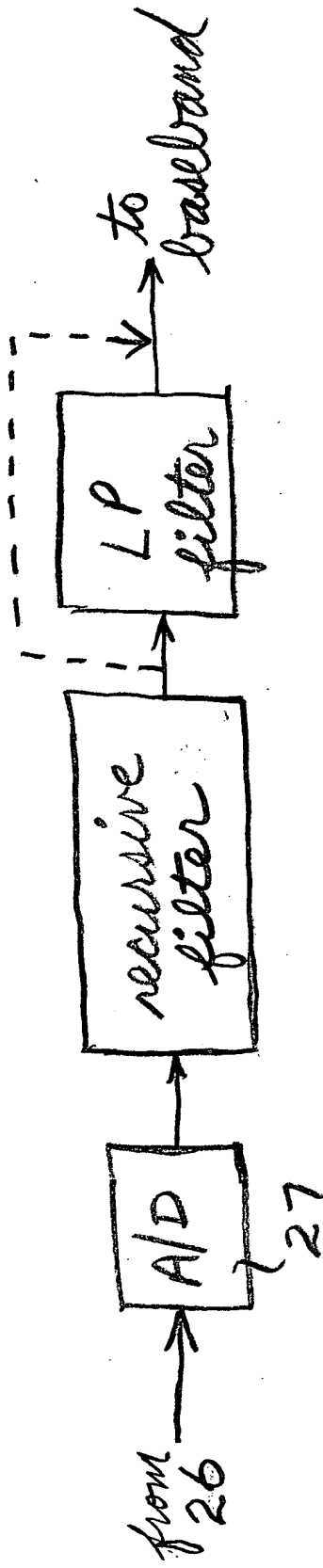


FIG. 22

FIG. 23 is a schematic diagram of a signal processing circuit. The circuit includes a DCU (Data Conversion Unit) connected to a grid of 8 columns and 8 rows of transistors. The columns are labeled 1 through 8. The rows are labeled 1 through 8. The grid is connected to a feedback loop consisting of a resistor R and a capacitor C. The feedback loop is connected to an op-amp (25) which is connected to the DCU. The op-amp output is connected to an A/D converter. The circuit is powered by a DC voltage source (20) and a reference voltage source (22). The reference voltage source is connected to the grid through a resistor (21). The feedback loop is connected to the grid through a resistor (31). The grid is connected to ground through a resistor (23).

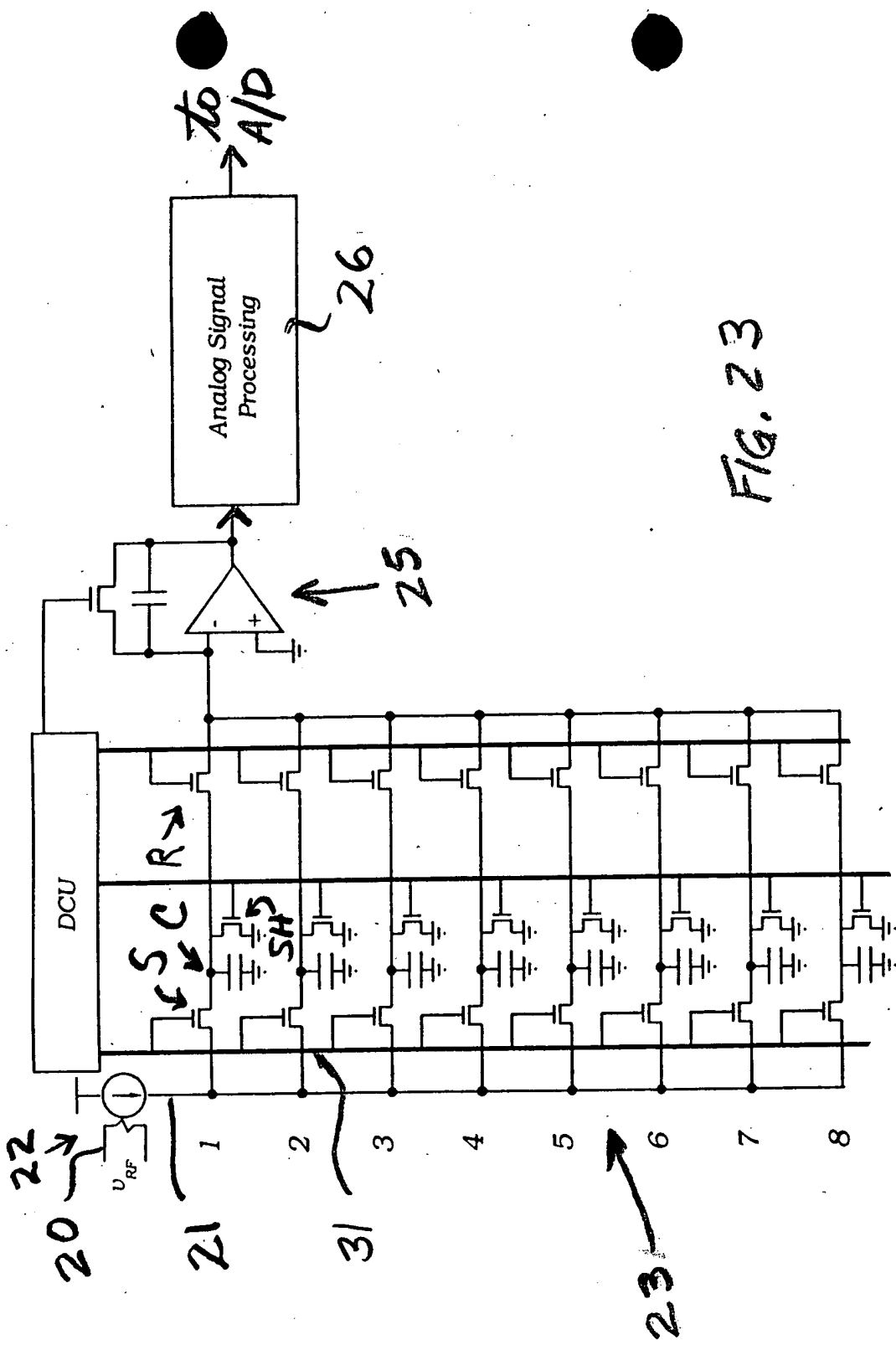


FIG. 23

FIG. 24

Samples	Switches Closed
$SA1, SA3, \dots, SAJ-1$	$S1$
$SA2, SA4, \dots, SAJ$	$S2$
$SAJ+1, \dots, SA2J-1$	$S3$ and $R1$ and $R2$
$SAJ+2, \dots, SA2J$	$S4$ and $R1$ and $R2$
$SA2J+1, \dots, SA3J-1$	$S1$ and $R3$ and $R4$
$SA2J+2, \dots, SA3J$	$S2$ and $R3$ and $R4$
$SA3J+1, \dots, SA4J-1$	$S3$ and $R1$ and $R2$
$\vdots$	$\vdots$

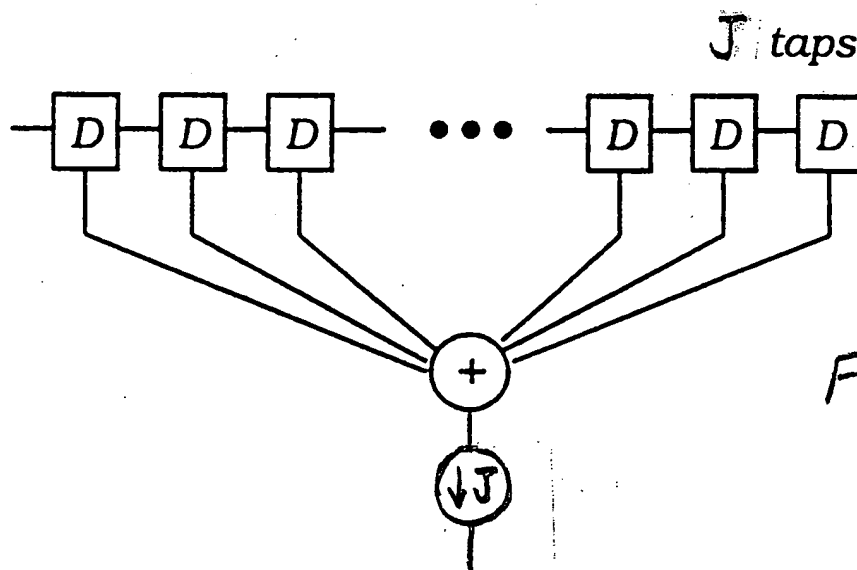


FIG. 25

SECRET

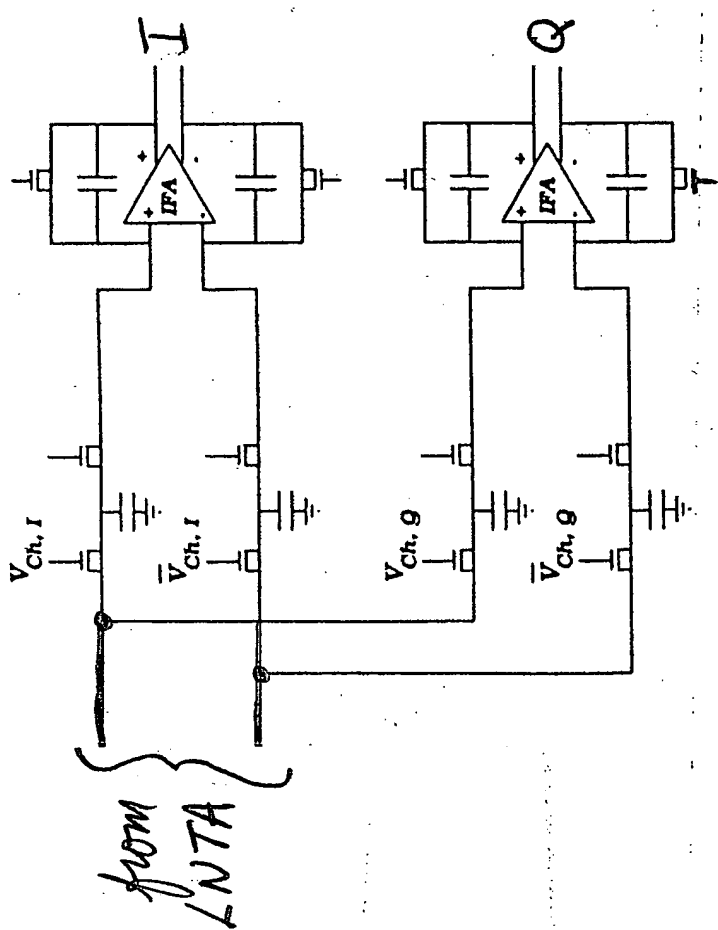


FIG. 26

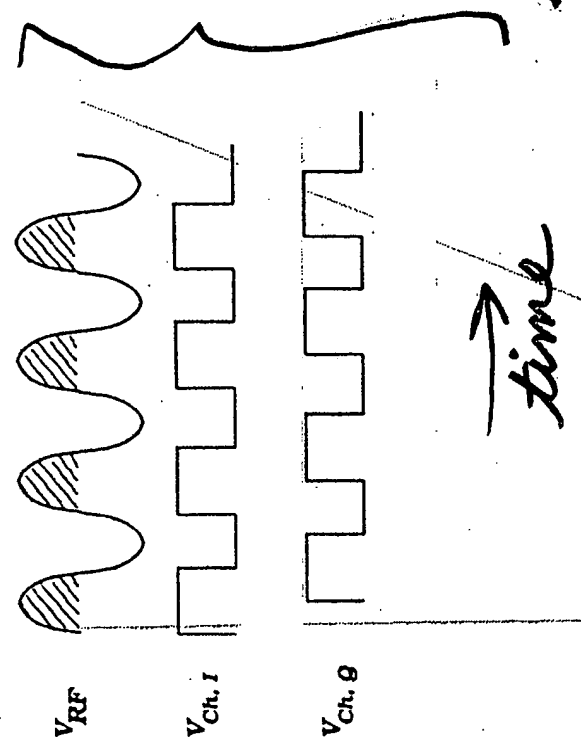


FIG. 27